Name: Mr. Pradipta Sarkar

Department: Electronics & Communication Engg

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Qualifications: M.Tech in Microelectronics & VLSI Engg

                B.Tech in Electronics & Communication Engg

Designation: Assistant Professor

VIDWAN ID: 184921

Experience (Teaching / Research / Industry, in years):

 6 years 10 months in Teaching and 2 years 4 months in Industry.

Date of Joining at the Present Institution: 12th January, 2016

Examinations Cleared:

Qualified **GATE** in 2009 in Electronics & Communication Engg

Qualified **NET-UGC** in 2018 in Electronic Science

Qualified **SET-WB** in 2018 in Electronic Science

Qualifications Summary (Reverse chronological order):

Degree	Institute	From – To	Subjects
Master of Technology (M.Tech)	National Institute of Technology, Durgapur.	2009-2011	Microelectronics & VLSI Engg
Bachelor of Technology (B.Tech)	Birbhum Institute of Engineering & Technology (WBUT)	2005-2009	Electronics & Communication Engg
Class 12	Barasat M.G.M High School (WBCHSE)	2002-2005	Science Group- Maths, Physics, Chemistry, Statistics.
Class 10	Barasat M.G.M High School (WBBSE)	2002	

Experience Summary (In chronological order):

Designation	Organization	Date–From	Date-To
Assistant Professor	Dr. B.C. Roy Engineering College, Durgapur	January, 2016	Present
Assistant Professor	National Institute of Science & Technology, Berhampur	April, 2014	March, 2015
Sr. Software Engineer	Samsung	April, 2013	January, 2014
Project Engineer	Wipro Technologies	August, 2011	March, 2013

Specialization/Research Interest:
Digital VLSI Design.

Awards & Recognitions:
Received GATE Scholarship of Rs. 8000 per month from MHRD during M.Tech.

Courses Taught:
B.Tech:
Theory:
Microelectronics & VLSI Engg,
Digital Image Processing,
Digital Signal Processing,
Analog & Digital Electronics,
Digital Communication,
Basic Electronics**.**
Lab:
Microelectronics & VLSI Engg Lab,
Digital Signal Processing Lab,
Analog & Digital Electronics Lab
Digital Communication Lab
Basic Electronics Lab
Digital Electronics Lab
Signal & System Lab
Microprocessor Lab,
Electronics Devices Lab

M.Tech:
Theory:
Error Control Coding
Lab: NIL

Online Mode of Teaching:

Live Classes using Google Meet are conducted on regular basis.
PPT based Class Notes are sent to students on regular basis.
Handwritten Class Notes are sent to students.
Learning Content available in the Internet were sent to the students.
MCQ questions using Google Quiz was send to the students.
MCQ based Real Timed Test was conducted using Google Quiz.
Assignments on descriptive questions was emailed to the students.

Digital Communication- My Handwritten Notes Links:

Constellation Diagram:

https://drive.google.com/file/d/1MMY4MZk-qN4xrCJ2O42FmSBeBZTHISB3/view?usp=sharing

Binary Amplitude Shift Keying:

https://drive.google.com/file/d/1xjfgi4ZSyWx78P_8xC5UZNAq6gOM9nsH/view?usp=sharing

Binary Frequency Shift Keying:

https://drive.google.com/file/d/1reZhqNdQE9mFaHQsOWahWznbHK2WIZpe/view?usp=sharing

Binary Phase Shift Keying:

https://drive.google.com/file/d/1MMY4MZk-qN4xrCJ2O42FmSBeBZTHISB3/view?usp=sharing

Differential Phase Shift Keying:

https://drive.google.com/file/d/1I3YlDkF52Nw_Vs9RfYT14_HvFWYOi2OH/view?usp=sharing

Quadrature Phase Shift Keying:

https://drive.google.com/file/d/13R62-mGogdjveb9XHrSqDXz2JUOKmuhA/view?usp=sharing

Probability Distribution:

https://drive.google.com/file/d/15qFRnEseWdtDHQmmbSU31xWV1DizmibY/view?usp=sharing

Uniform Probability Distribution:

https://drive.google.com/file/d/1yDoal3uc8FadGcalnqB8ZTVbLSmIsAas/view?usp=sharing

Gaussian Probability Distribution:

https://drive.google.com/file/d/1ZYwmYyHUawjhbdvDDZAL7NWc5X3kIF3E/view?usp=sharing

Joint Probability Distribution:

https://drive.google.com/file/d/13SiisGUCKUkki9LKZ1miid4g1KIIGesT/view?usp=sharing

Orthogonal Signals:

https://drive.google.com/file/d/1ElQWTrEJ1dtRn2q6SzC1tjnyUFlCbiNX/view?usp=sharing

Publications: NIL
Journal: NIL
Conference: NIL
Book: NIL

Supervision of Ph.D/M.Tech / B.Tech Projects:
Ph.D: NIL
M.Tech: NIL

B.Tech Projects:

YEAR	PROJECT TITLE	STUDENT'S NAME	UNIV ROLL NO
2020	Digital IC Tester using Audrino	Shuvam Kabiraj	12000316036
		Souvik Bhunia	12000316030
		Supriyo Pal	12000316016
		Divyansh Divyam	12000316106
		Fatema Khatun	12000316105
		Bikash Roy	12000316111
2019	Line Follower Robot with collision avoidance using Bluetooth module.	Monisha Mondal	12000315063
		Md Ashif Khan	12000315058
		Raktim Kumar Nandi	12000315077
		Ritika Das	12000315083
		Riti Kumari	12000315082
		Sankarshan Chatterjee	12000315092
2018	Hardware Design of Reed Solomon Encoder Decoder.	Amresh Kumar	12000314010
		Om Prakash	12000314060
		Sonu Shrikant	12000314096
		Tabrej Alam	12000315135
		Chandan Kumar Singh	12000314026
2017	I2C Bus Protocol Hardware Design	Abhishek Kumar	12000313006
		Amit Kumar	12000313012
		Ananya Sarkar	12000314119
		Kumar Sanu	12000313043
		Monis Jamal	12000313049
		Trisha Bhattacharjee	12000314136
2016	UART Hardware Design.	Amrit Acharya	12000312012
		Dheeraj Kumar Singh	12000312039
		Ankur Srivastava	12000312019
		Shouvik Singha	12000313138
		Raushan Kumar Thakur	12000312075
		Rahul Ranjan	12000312069

Invited Lectures: NIL

Participation in seminar/conference/symposium/workshop/discussion meeting:
Workshop of Full Custom VLSI Design conducted by CoreEl Technologies and held at Dr. B.C. Roy Engineering College held in June, 2019.

Participation in faculty development programmes:
FDP on Behavioural Modelling & Use of ICT Tools conducted by IIT-Guwahati and held at Dr. B.C. Roy Polytechnic, Durgapur from 14th January to 19th January, 2019.

Organization of events (Dr. B. C. Roy Engineering College): NIL

Participation in administrative committees (selected)
Presently working as Departmental Placement Coordinator of Electronics & Communication Engg. Dept. in Training & Placement Cell since July, 2018.

Project Ideas Submitted to Govt. Agencies/ On-going Projects / Research Ideas under preparation & execution: NIL

Membership of professional bodies: NIL