



Faculty Development Programme  
on  
**Embedded System Design using FPGAs**  
*In association with Intel Pvt. Ltd.*  
at  
*National Institute of Technology, Meghalaya*



**Certificate of Participation**

This is to certify that **Mr./Ms./Dr. MOLOY MUKHERJEE** from **DR.B.C.ROY ENGINEERING COLLEGE** has participated in five days online Faculty Development Programme on “**Embedded System Design using FPGAs**” in association with Intel Pvt. Ltd. organized by Department of Electronics and Communication Engineering, NIT Meghalaya held from 4<sup>th</sup> – 8<sup>th</sup> April, 2022.

**Dr. P Rangababu**  
**FDP Convenor, NIT Meghalaya**

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