

পশ্চিমবঙ্গ পশ্চিম বঙ্গাল WEST BENGAL

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**MEMORANDUM OF AGREEMENT**  
Between

**Dr. B. C. Roy Engineering College, Durgapur**  
And

**IC Design and Fabrication Centre,  
Department of Electronics & Tele-communication, Jadavpur University**

This agreement is executed on 03/06/2024

BETWEEN

**Electronics and Communication Engineering Depttment (ECE) of Dr. B.C. Roy Engineering College, Durgapur, Fhul Jhor, Jemua Road, Durgapur - 713206 of the FIRST PART**

AND

**IC Design & Fabrication Centre, Electronics & Telecommunication Engineering Department, Jadavpur University, a body corporate under JU Act, 1981, having its campus at Jadavpur University, Kolkata – 700032, India (hereinafter referred to as JU) of the SECOND PART.**

WHEREAS JU, as part of achieving one of its objectives, has undertaken to train a group of young people in 'VLSI Design & Microelectronics Technology and Embedded system and IoT' to serve the community.



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No. ....  
Name : A. K. SINGH, Advocate  
Address : High Court, Calcutta  
Kolkata - 700001

Rs. ....  
Kolkata Collectorate  
11, Netaji Subhas Rd.,  
Kolkata-1

Amal Kr. Saha  
Licensed Stamp  
Vendor

02 MAY 2024

AND WHEREAS the representative of Dr. B. C. Roy Engineering College, Durgapur approached the Co-ordinator, IC Design and Fabrication Centre under Department of Electronics & Tele-communication Engineering of JU to offer one month Certificate course in **VLSI Design & Microelectronics Technology** and/or '**Embedded Systems and IOT**' (hereinafter referred to as the course) from the session starting from 01-06-2024 and also to finalize the terms of this agreement.

AND WHEREAS pursuant to the said discussion, JU has agreed to offer the **Certificate Course in VLSI Design & Microelectronics Technology** and/or '**Embedded Systems and IOT**' exclusively from the academic session **1st June 2024 to 31st May 2028** for a period of four (4) years with at least one programme per year.

IN WITNESS WHEREOF, the parties hereto agree to the terms and conditions as hereunder written.

- 1] The said course will be a part-time (6 hours/day) in nature. Certificate will be awarded to only those candidates who shall successfully complete the course.
- 1a] The proposed certificate course will be arranged by the IC Design & Fabrication Centre, Department of Electronics & Telecommunication Engineering, JU and will be held at IC Design & Fabrication Centre only.
- 2] The candidate should be either BE/B.Tech (in Electronics/Computer Science any allied branch involving machine learning, AI also) / Instrumentation / Electrical Engineering) or M.Sc. / B.Sc.(Hons) (in Electronics / Computer Science) or equivalent (e.g. AMIE/AMIETE etc.).
- 3] There shall be a **Course Committee** consisting of the following members:
  - (i) Co-ordinator, IC Centre, JU
  - (ii) Co Coordinator, IC Centre, JU
  - (iii) HOD, Dept. of ETCE, JU
  - (iv) Principal/HOD (ECE), Dr. B.C. Roy Engineering College, Durgapur.
  - (v) Co-ordinator of VLSI Lab., Dr. B.C. Roy Engineering College, Durgapur.
  - (vi) Co-Coordinator of VLSI Lab, Dr. B.C. Roy Engineering College, Durgapur.
- 4] Course fee is payable at the beginning of the course as per Table-1.
- 5] The Course Committee shall look after the following:
  - (i) Framing of syllabus and making future amendments/additions/alternations etc., as and when necessary.
  - (ii) Looking after the academic administration of the course including recommending names of faculty members to Co-ordinator, IC Design and Fabrication Centre, JU, the faculty members shall be identified by IC Design and Fabrication Centre, JU.
  - (iii) The Institute/Organization stated as First Part in the MOA should arranged at least one batches of students for training in a year and 2nd part IC Design & Fabrication Centre, JU should also agree in conducting two training program in a year.

- 6] IC Design and Fabrication Centre, JU, shall conduct all activities relating to examination corresponding to their respective teaching load. These include printing of question papers, conducting of examination.
- 7] Format of the Course certificate shall be approved by the Course Committee. The authorized representatives of JU (Head of the Department, ETCE Dept., JU and Co-ordinator of IC Design and Fabrication Centre, JU) jointly sign the certificate to be issued to the students. The certificate shall be prepared by IC Design and Fabrication Centre, JU and shall be distributed by IC Design and Fabrication Centre, JU.
- 8] Once students have been admitted, IC Design and Fabrication Centre, JU shall be responsible for providing all types of cooperation needed for conducting the course.

### **COLLABORATION**

- (i) The entire financial obligation, both administrative and academic – expenditure for running the aforesaid course – shall be met up from the course fees collected from the students admitted to the course.
- (ii) IC Design and Fabrication Centre shall collect the fees from the students by DD, in favour of "**REGISTRAR, JADAVPUR UNIVERSITY**" and shall pay to JU after deduction Overhead 10% of the fees & 18% GST paid to JU and release the balance in favour of "**IC DESIGN & FABRICATION CENTRE, JU**".
- (iii) The cost of marketing the course including the cost of marketing personnel, advertisement(s) in print and audio / visual media, if any, brochures, handouts, leaflets, etc. shall be met up from the course fees collected from the students admitted to the course. The legal expenses, if any, incurred for defending the suit(s) instituted by any of the students and/or other parties in connection with the said Course shall also be debited to this account.
- (iv) The expenditure as may be incurred for paying remuneration to the Faculty Members / Teachers / Technical staff, Attendants etc should be met up from the course fees collected from the students admitted to the course.
- (v) Expenditure to be incurred for conducting examinations shall be met up from the course fees collected from the students admitted to the course.

This agreement will remain in force initially for **Four year** from the date of signing of this agreement. This agreement may be renewed on its expiry by a written request by the 1st party at least two months prior to the expiry. The first or any subsequent annual renewal shall not take place, if notice in writing of its intention not to renew this agreement is given by either party to the other at least two months in advance.



If any dispute arises between the parties of the FIRST AND SECOND PART the same shall be decided upon arbitration of the Vice-Chancellor, Jadavpur University and his/her decision shall be final.

### **About the Programme**

- 1] The Centre will train the students of the department in the domain of VLSI Design & Microelectronics Technology and/or 'Embedded Systems and IOT' for a duration of 100 hours (approximately) per session probably in the inter semester gap. The cost of the training will be borne by the Institute (Dr. B.C. Roy Engineering College, Durgapur) as per the following rate:-

**Table - 1: Course Fee Structure**

No. of Students	Cost of the Training Programme
61 - 80	@ Rs. 5,310/- [Rs. 4,500/- + 18% GST] Per head
21 - 60	@ Rs. 5,900/- [Rs. 5,000/- + 18% GST] Per head
01 - 20	@ Rs. 8,260/- [Rs. 7,000/- + 18% GST] Per head


- 2] Students of M.Tech (VLSI Design/ or any allied course involving fabrication or system design) can pursue fabrication and integration part of their final semester project at the centre at the cost of Rs. 7,500/- per head + 18% GST.
- 3] Students of B.Tech (VLSI Design/ or any allied course involving fabrication or system design / AI / IoT) can pursue design and fabrication part of their final semester project at the centre at the cost of Rs. 7,500/- per head + 18% GST
- 4] Two staff (Faculty members and/or Technical support staff) of the department per year can pursue training at the centre at free of cost, provided two batches of at least 10 students attending training program.
- 5] Both parties can prepare joint collaboration project proposal and respective procedural aspects and financial terms and condition (budget preparation) will be decided during the proposal submission and PIs of the project will be decided with mutual understanding.
- 6] Faculty Development Program for the favour of the 1st party i.e. (Dr. B.C. Roy Engineering College, Durgapur) can be organized by IC Design & Fabrication Centre, JU where the man power and resource of the department and the centre will be utilized. Modality of the program and fees for the Faculty Development Programme will be decided based on the requirement of the program.

- 7] The agreement is valid for the academic session (2024-2028) but can be terminated by either side with valid justification after a particular academic session (not in the midsession).

### FORCE MAJEURE

- (i) In the event either party cannot perform its respective obligation under the Agreement due to Force Majeure, the corresponding obligations of the other party may at the instance and notice of such either party be suspended for a similar period of time.
- (ii) Force Majeure shall include but not limited to acts of God, regulation of Government, earthquakes, floods, epidemic, war, revolution, strikes, lockouts, explosion, computer virus attack and natural calamities.

Signed on behalf of Second Part  
/ Jadavpur University

  
HOD ETCE, JU 03/06/24  
SECOND PART

**MANOTOSH BISWAS**  
Professor and Head  
Electronics and Telecommunication Engineering  
Jadavpur University, Kolkata - 32

Signed on behalf of First Part  
/ Coordinator of VLSI Centre for  
Excellence Lab Dr. B.C. Roy  
Engineering College, Durgapur




Principal / HOD  
Dr. B.C. Roy Engineering  
College, Durgapur  
FIRST PART

**Principal**  
**Dr. B. C. Roy Engineering College**  
**DURGAPUR**

### **Witnesses:**

1.   
Co-ordinator, IC Centre,  
Jadavpur University.  
**Prof. Sayan Chatterjee**  
Co-ordinator  
IC Design & Fabrication Centre  
Dept. of ETCE., JU

2.

1.   
HOD, Dr. B.C. Roy Engineering  
College, Durgaur.

Head  
Dept. Electronics & Comm. Engg.  
Dr. B. C. Roy Engineering College  
Durgapur.

2. 

3. 